

<b>Notice of References Cited</b>	Application/Control No. 10/717,337	Applicant(s)/Patent Under Reexamination BENEDIX ET AL.	
	Examiner Sam Dillon	Art Unit 2185	Page 1 of 1

**U.S. PATENT DOCUMENTS**

*		Document Number Country Code-Number-Kind Code	Date MM-YYYY	Name	Classification
	A	US-			
	B	US-			
	C	US-			
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	E	US-			
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**FOREIGN PATENT DOCUMENTS**

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**NON-PATENT DOCUMENTS**

*		Include as applicable: Author, Title Date, Publisher, Edition or Volume, Pertinent Pages)
	U	Mekhiel et al., "Performance analysis for a cache system with different DRAM designs", 1993, IEEE CCECE/CCGEI '93, pp. 365-368
	V	
	W	
	X	

\*A copy of this reference is not being furnished with this Office action. (See MPEP § 707.05(a).)  
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